

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,943,608 B2  
DATED : September 13, 2005  
INVENTOR(S) : Kelkar

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9,

Line 53, delete "or" and insert -- of --.

Column 10,

Line 9, delete "on" and insert -- on a --.

Line 26, delete "of gate" and insert -- of a gate --.

Line 29, delete "terminal or" and insert -- terminal of --.

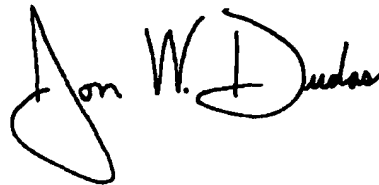
Line 35, delete "transistor gate" and insert -- transistor --.

Column 12,

Line 19, delete "cross-output circuit" and insert -- cross-output transistor circuit --.

Signed and Sealed this

Twenty-ninth Day of November, 2005

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*